



Curriculum Vitae Europass

Personal information

Name/Surname **Graziano Pravadelli**
E-mail graziano.pravadelli@univr.it

Nationality Italian

Date of birth

Gender Male

Desired employment / Occupational field

Teaching and research activity in the field of design and verification of embedded systems and cyberphysical systems applied to healthcare, wellbeing and industrial scenarios
Organizational activity in the field of quality assurance of the university system

Working experiences

Dates October 2018 →

Occupation or position held Full professor of information processing systems (ING-INF/05).

Main activities and responsibilities Research: Design and verification of embedded and cyberphysical systems, design of IoT-based virtual coaching systems, telemedicine, human activity recognition, wireless body area networks.
Teaching: Operating systems, advanced operating systems, embedded operating systems, system verification and testing, Basic computer science

Name and address of the employer Università di Verona, Via dell'Artigliere 8, 37129 Verona. www.univr.it

Type of business or sector University

Dates July 2007 →

Occupation or position held Co-founder and co-owner

Main activities and responsibilities HR management, project management for IoT and healthcare

Name and address of the employer EDALAB s.r.l., Via Ca' Nova Zampieri 12, 38053 S. Giovanni Lupatoto (VR), www.edalab.it

Type of business or sector ICT

Dates July 2015 →

Occupation or position held Evaluation expert

Main activities and responsibilities Quality assessment of Italian universities

Name and address of the employer Agenzia Nazionale di Valutazione del Sistema Universitario e della Ricerca (ANVUR), Via I. Nievo 35, Roma

Type of business or sector University

Dates July 2017 → November 2021

Occupation or position held President of the quality assurance board

Main activities and responsibilities Organization of quality promotion activities at the University of Verona.

Name and address of the employer Università di Verona, Via dell'Artigliere 8, 37129 Verona

Type of business or sector University

Dates January 2011 → September 2018
 Occupation or position held Associate professor of information processing systems (ING-INF / 05).
 Main activities and responsibilities Research: Design and verification of embedded systems.
 Teaching: Operating systems, Advanced operating systems, Software for embedded systems, Basic computer science
 Name and address of the employer Università di Verona, Via dell'Artigliere 8, 37129 Verona
 Type of business or sector University

Dates January 2005 – December 2010
 Occupation or position held Assistant professor of information processing systems (ING-INF/05).
 Main activities and responsibilities Research: Design and verification of embedded systems.
 Teaching: Operating systems, Advanced operating systems, Software for embedded systems, Basic computer science
 Name and address of the employer Università di Verona, Via dell'Artigliere 8, 37129 Verona
 Type of business or sector University

Dates January 2004 – December 2004
 Occupation or position held Research fellow
 Main activities and responsibilities Design and verification of embedded systems, tutoring activities for Computer Science degree courses
 Name and address of the employer Università di Verona, Via dell'Artigliere 8, 37129 Verona
 Type of business or sector University

Education and training

Dates September 2008 – May 2009
 Title of qualification awarded FlxO Action 4 Program "Training and accompanying actions for the launch of academic spin-offs oriented towards the industrial use of university research results"
 Principal subjects/occupational skills covered Strategic factors of competitiveness, marketing, administration and management control, banking, tax, management and organization, corporate law, contracts, protection of intellectual property and patents
 Name and type of organisation providing education and training Fondazione CUOA, Altavilla Vicentina

Dates May 2001 – April 2004
 Title of qualification awarded Phd in Computer Science
 Principal subjects/occupational skills covered Writing research projects, writing scientific articles, design and verification techniques for embedded systems.
 Name and type of organisation providing education and training Università di Verona
 Level in national or international classification PhD

Bibliometric indexes

Scopus
 H-index: 17
 Total citations: 1083
 Total indexed contributions: 135

Awards

Date December 2019
 Award Best FSE project award for "BIPBIP: a smart wearable system to prevent freezing in people with Parkinson's disease".

Promoter Veneto Region

Date December 2017

Award National prize for innovation. Special mention as the best social innovation project for “ADA – assisting daily life activities, an ICT-based virtual coaching system to support elderly in activities of daily life”.

Promoter PNICube (www.pnicube.it)

Personal skills and competencies

Mother tongue **Italian**

Other language
Self assessment
European level ()*

English

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
B2	Independent user	C1	Proficient user	B2	Independent user	B2	Independent user	C1	Proficient user

(*)Common European Framework of Reference (CEF) level

Social skills and competencies Excellent communication skills derived from twenty years of experience in the teaching sector. Excellent team working ability gained during the presidency of the quality assurance board of the University of Verona and the coordination of research projects.

Organizational skills and competencies Ability to work in situations of stress accrued in the face of activities with non-postponable deadlines. Excellent sense of organization gained in all professional experiences.

Technical skills and competencies Design, simulation and semi-formal verification of embedded HW / SW and cyber-physical systems, in particular as regards virtual prototyping, automatic generation of embedded SW correct for construction, TLM and RTL modelling, abstraction and refinement techniques, fault simulation and test generation, functional qualification and assertion-based verification.
Design of virtual coaching systems based on smart objects and machine learning techniques for ambient assisted living to support people with special needs
European / national / industrial project management. Writing scientific articles and teaching texts. Translation of scientific books.
Design and management of computational thinking laboratories for primary schools.

Computer skills and competencies Operating systems: Windows, Linux and Mac OS X. Programming languages: C / C ++, SystemC, VHDL. Google Suite for Education and related tools. Several applications for text manipulation, spreadsheets, internet browsing, project management. Several applications for design, verification and simulation of embedded systems.

Project coordination in the healthcare field	<p>Tele-monitoring, physical therapy, and nutrition in neurology (Parkinson's disease, chemotherapy-induced neurotoxicity, emotional eating disorders) Funding organization: Brain Research Foundation Period: May 2021-April 2024 Achieved contribution: € 90000</p> <p>Title: Smart-PUMP: a smart assistive system for monitoring and controlling motor fluctuations in Parkinsonians Funding organization: Veneto Region Period: July 2020 – June 2021 Achieved contribution: € 47100</p> <p>Title: BIPBIP: a wearable smart system to prevent freezing of gait in people affected by Parkinson's disease Funding organization: Veneto Region Period: October 2018 – September 2019 Achieved contribution: € 74500</p> <p>Title: FOLLOWME: an interactive platform to support guided tours in telepresence Funding organization: Veneto Region Period: September 2016 – October 2017 Achieved contribution: € 41950</p> <p>In addition, Graziano Pravadelli coordinated and participated to 20+ industrial and research projects at national and international level concerning the design and verification of embedded and cyber-physical systems.</p>
Scientific services	<p>Chair of the IFIP Working Group 10.5 on design and engineering of electronic systems since 2020; Chair of the steering committee of IFIP/IEEE VLSI-SOC conference since 2020; Program chair of the IEEE Smart and Connected Health Symposium in 2022; Member of the steering committee of IFIP/IEEE VLSI-SOC conference from 2018 to 2020; General chair of IFIP/IEEE VLSI-SOC conference in 2018 and 2022; Member of the steering committee of IFIP/IEEE FDL conference since 2017; Finance chair of ACM/IEEE NOCS conference in 2014, and IFIP/IEEE FDL conference in 2017 and in 2018; Member of the executive committee of ACM/IEEE DATE conference since 2020; Track chair at ACM/IEEE DAC conference from 2019 to 2021; Track chair at ACM/IEEE DATE conference from 2018 to 2020; Track chair at IFIP/IEEE VLSI-SOC conference in 2015, 2016, 2017, 2019, 2020, 2021; Member of the program committee of several international conferences; Referee activities for 50+ international conferences and journals; Organizer and speaker of tutorials and special sessions at several international conferences.</p>
Artistic skills and competencies	Guitar (self-taught)
Other skills and competencies	Swimming, skiing, tennis.
Driving license	Driving license B.
Additional information	More than 100 publications in the field of design and verification of embedded systems (see https://www.di.univr.it/?ent=persona&id=123)

01/04/22

I authorize the processing of personal data contained in the curriculum vitae based on art. 13 of Legislative Decree 196/2003 and art. 13 GDPR 679/2016. Aware that false declarations entail the application of the penalties provided for by art. 76 of the Presidential Decree

445/2000, I declare that the information reported in the following curriculum vitae, drawn up in European format, is true (Art.45, 46, 48, 76 DPR 28/12/00 n.445 and as amended).