

CV SUMMARY

PROF. GIANLUCA PALERMO

1. GENERAL INFORMATION

CAREER

2015 – now Associate Professor, Politecnico di Milano, Italy
2008 – 2015 Assistant Professor, Politecnico di Milano, Italy
2006 – 2007 Research fellow, Advanced Learning and Research Institute (ALaRI) – USI, Lugano, Switzerland
2006 – 2008 Post-Doc Researcher (Assegnista di Ricerca), Politecnico di Milano, Italy
07/2006 Visiting Researcher, STMicroelectronics AST- R&I, Grenoble, France
07-08/2005 Visiting Researcher, STMicroelectronics AST- R&I, Grenoble, France
2003 – 2006 Ph.D. Student, Politecnico di Milano, Italy
2002 – 2003 Consultant engineer, Low Power Design Group of AST - R&I – STMicroelectronics, Italy

NATIONAL HABILITATION

Type of habilitation	Country	SSD (if Italian habilitation) or topic area	Date of achievement
Full Professor	Italy	ING-INF/05	13/11/2020

RESEARCH INTERESTS

- **Electronic Design Automation:** System Level Modeling, Design Space Exploration of Multi/Many-Core Architectures, Networks-on-Chip Architectures, Technology-aware architecture design.
- **Energy Efficient Computing:** Application & Compiler Autotuning, Parallel Applications, Approximate Computing, GPU.
- **In Silico Drug Design:** Virtual Screening, Molecular Docking, High Performance Computing, Urgent Computing.

2. QUALITY OF SCIENTIFIC AND/OR PROJECT PRODUCTION

PRODUCTIVITY AND IMPACT METRICS

- **Scientific Productivity:** 149 publications (159¹ entries and 226 co-authors according to Scopus).
 - Author/Co-author of 34 journal papers including 12 top-ranked Q1 journals
 - Author/Co-author of 100 scientific publications on peer-reviewed conferences (1 A++/A+ Class and 29 A/A- Class);
 - Inventor/Co-inventor of 2 patent applications with STMicroelectronics (1 out of 2 already granted).
- **Publication Impact:** Based on Google Scholar: h-index 31 citations 3432
Based on Scopus: h-index 24 citations 1860

AWARDS AND RECOGNITIONS

2020 HiPEAC Technology Transfer Award 2020
2020 IEEE Senior Membership
2020 ACM Service Award as General Chair of the ACM International Conference on Computing Frontiers
2016 ACM Service Award as General Chair of the ACM International Conference on Computing Frontiers
2010 HiPEAC Paper Award as co-author of the paper published at DAC2010
2006 Dimitris N. Chorafas Foundation Award - Carlo Pesenti Prize for the PhD Thesis

INVITED TALKS AND SEMINARS (SELECTION OF PAST 10-YEARS)

2014 - Invited talk at the International Symposium on Applied Reconfigurable Computing, 2014
2017 - Invited talk at the HPCAFE-2017 (European HPC Summit Week)
2017 - Panel Member at the ACM International Conference on Computing Frontiers
2018 - Invited Talk at the ACM International Conference on Computing Frontiers
2018 - Seminar at the CPS Summer School - Designing Cyber-Physical Systems From concepts to implementation, 2018
2018 - Seminars at PRACE On Demand Training Event/School on Energy-Efficient HPC Systems

¹ The higher number in Scopus is because in case of books it includes each chapter as a different entry and includes also the message from chairs in the proceedings.

2019 - Seminar at the NIPS Summer School - Architectures and Algorithms for Energy-Efficient IoT and HPC Applications
 2020 - Seminar at the CECAM Seminars series “The importance of being H.P.C. Earnest”
 2020 - Invited Talk at the ESTECO User Meeting Conference, 2020.
 2021 - Panel Member at the on-line Webinar on Energy reduction for AI workloads

INSTITUTIONAL RESPONSIBILITIES (SELECTION OF PAST 10-YEARS)

2020 – present PoliMi representative within the EIT-Digital association
 2008 – present Responsible for the foreign students within the CSE track

ORGANIZATION OF SCIENTIFIC MEETINGS (SELECTION OF PAST 10-YEARS)

2010 - 2011 Track Chair, PARMA - Workshop on Parallel Programming & Run-Time Management
 2013 - 2017 Program Chair, Workshop on Rapid Simulation and Performance Evaluation: Methods and Tools
 2014 Track Chair, EUC - International Conference on Embedded and Ubiquitous Computing
 2015 Program Chair, International Workshop on Reconfigurable Computing for HPC and HPDA
 2016 General Chair, International Conference on Computing Frontiers
 2017 Track Chair, CODES+ISSS - International Conference on Hardware/Software Codesign & System Synthesis
 2019 Program Chair, Workshop on Rapid Simulation and Performance Evaluation: Methods and Tools
 2020 General Chair, International Conference on Computing Frontiers
 2020 - 2021 Topic Co-Chair, DATE - Design Automation and Test in Europe
 2022 Topic Chair, DATE - Design Automation and Test in Europe (in preparation)
 2016 - now Steering Committee Member, International Conference on Computing Frontiers
 2006 - now Member of the TPC for several conferences in my research field

PARTICIPATION TO EDITORIAL BOARDS (SELECTION OF PAST 10-YEARS)

2019 – now Editorial Board Member of Journal of System Architecture – Q1 2020
 2020 – now Associate Editor of Electronics, Computer Science and Engineering section – Q2 2020
 2016 – 2018 Guest Editor of International Journal of Parallel Programming – Q3 2016-2018

COMMISSIONS OF TRUST (SELECTION OF PAST 10-YEARS)

2011 - Panel member for the Ministry of Economy and Finance (MEF), AAMS – AWP/VLT selection procedure
 2012 - Panel member for the Academy of Finland for the evaluation of research project proposals
 2013 - Panel member for the Academy of Finland for the evaluation of research project proposals
 2013 - External Reviewer for the French National Research Agency (ANR)
 2015 - External Reviewer for the Singapore Ministry of Education (MOE)
 2016 - Panel member for the Academy of Finland for the evaluation of research project proposals
 2017 - Panel member for the Academy of Finland for the evaluation of research project proposals
 2018 - Chair of the Panel for the Academy of Finland for the evaluation of research project proposals
 2019 - Member of the recruitment committee at PoliTo for 1 Position as RTD-A
 2021 - Member of the recruitment committee at UniCusano for 1 Position as RTD-A

3. TEACHING ACTIVITIES

COURSES WITH A PRIMARY RESPONSIBILITY (SELECTION OF PAST 10-YEARS)

Course name	Reference Study Course	Time period
Energy Efficient and Approximate Computing	CSE – PhD Level	AA 20/21
Computing Infrastructure	CSE – Master Level	AA 20/21
Reti Logiche + Progetto di Reti Logiche	CSE – Bachelor Level	AA 20/21
Computing Systems	Eng. Physics – Master Level	AA 19/20
Reti Logiche + Progetto di Reti Logiche	CSE – Bachelor Level	AA 19/20
Computing Systems	Eng. Physics – Master Level	AA 18/19
Reti Logiche + Progetto di Reti Logiche	CSE – Bachelor Level	AA 18/19
Computing Systems for Engineering Physics	Eng. Physics – Master Level	AA 17/18
Reti Logiche + Progetto di Reti Logiche	CSE – Bachelor Level	AA 17/18
Computing Systems for Engineering Physics	Eng. Physics – Master Level	AA 16/17
Reti Logiche	CSE – Bachelor Level	AA 16/17
Computing Systems for Engineering Physics	Eng. Physics – Master Level	AA 15/16
Reti Logiche	CSE – Bachelor Level	AA 15/16
Informatica per le applicazioni scientifiche e industriali	Eng. Physics – Master Level	AA 14/15

Informatica ed Elementi di Informatica Medica	Biomedical Eng. – Bachelor Level	AA 14/15
Informatica per le applicazioni scientifiche e industriali	Eng. Physics – Master Level	AA 13/14
Informatica ed Elementi di Informatica Medica	Biomedical Eng. – Bachelor Level	AA 13/14
Informatica per le applicazioni scientifiche e industriali	Eng. Physics – Master Level	AA 12/13
Informatica ed Elementi di Informatica Medica	Biomedical Eng. – Bachelor Level	AA 12/13

SUPERVISION OF MASTER, DOCTORAL STUDENTS AND POSTDOCTORAL RESEARCHERS

2010 – present	Advisor of 3 Doctoral Students, Co-Advisor of 2 Doctoral Students, Advisor/Co-Advisor of 3 Postdoctoral Researchers at Politecnico di Milano.
2019	Member of the final PHD Examination Committee at Politecnico di Milano
2020	External Member of the final PHD Examination Committee at University of Turku, Finland
2020	Member of the final PHD Examination Committee at University of Porto, Portugal
2020	Member of the final PHD Examination Committee at Politecnico di Milano
2021	Member of the PHD Examination Committee at VSB - Technical University of Ostrava, Czech Republic
2003 – present	Advisor/Co-advisor of >50 students within Politecnico di Milano, ALaRI – USI, and STMICROELECTRONICS facilities.

4. SCIENTIFIC RESPONSIBILITY FOR FUNDED PROJECTS

LEADERSHIP IN COMPETITIVE RESEARCH PROJECTS

Project Acronym	Time Period	Funding Institution	Funding Scheme	Role of the applicant
EXSCALATE4COV	2020-2021	EU-H2020	RIA	Principal Scientist of the Local Unit
EVEREST	2020-2023	EU-H2020	RIA	Work-Package/Task Leader
LIGATE	2021-2023	EU-EuroHPC+MISE	IA	Project Technical Coordinator
ANTAREX	2015-2019	EU-H2020	FET-RIA	Deputy-Coordinator & Task Leader
CONTREX	2013-2016	EU-FP7	IP	Task Leader
COMPLEX	2009-2013	EU-FP7	IP	Work Package/Task Leader
MULTICUBE	2008-2010	EU-FP7	STREP	Work-Package Leader
Computing Resources Oriented Project				
QA-SMU	2021-2022	CINECA	ISCRA-C	Principal Investigator
HPCaaS4VS	2021-2022	LEXIS	First Stage	Principal Investigator
HPCaaS4COVID	2020-2021	IT4I	Res.Allocation	PI for the PoliMi Unit

LEADERSHIP IN INDUSTRIAL-FUNDED RESEARCH PROJECTS

Project Acronym	Time Period	Funding Company	Role of the applicant
HPVS-4-UC	2020-2022	Dompé Farmaceutici	Principal Investigator

5. PUBLICATIONS

THE FULL LIST OF PUBLICATIONS CAN BE FOUND AT

- GOOGLE SCHOLAR: [HTTPS://SCHOLAR.GOOGLE.COM/CITATIONS?USER=Np_Q0dMAAAAJ&HL=EN](https://scholar.google.com/citations?user=Np_Q0dMAAAAJ&hl=en)
- DBLP: [HTTPS://DBLP.ORG/PID/87/565.HTML](https://dblp.org/pid/87/565.html)